



GOVERNMENT OF KERALA
DEPARTMENT OF TECHNICAL EDUCATION
COLLEGE OF ENGINEERING TRIVANDRUM

ENGINEERING COLLEGE P.O.
THIRUVANANTHAPURAM
KERALA, INDIA - 695 016

Tel: Office : 91-471-2515556
Principal: 91-471-2598370
Fax: 91-471-2515555
Email: cetvm@cet.ac.in
Website: www.cet.ac.in

Dated: July 08, 2024

Advertisement for the post of Junior Research Fellow (01)

Applications are invited from highly motivated candidates for the positions of Junior Research Fellows (JRFs) in sponsored project of ISRO under RESPOND scheme for the project entitled "**Precise Baseline/Orbit Determination (PBD/POD) Algorithm for Tandem Satellite operations for high precision GNSS Receiver**" to be implemented jointly by College of Engineering Trivandrum and ISRO Satellite Application Centre, Bangalore.

Aim of the Project: Determination of accurate baseline vector between the two satellites is critical for accurate height determination through tandem SAR signals. The height resolution depends on baseline that should be accurate to a few centimeters. Precise baseline/orbit determination (PBD/POD) is a crucial subsystem for Tandem SAR satellite operation. The system employs High Precision NavIC/GNSS Receiver using double differential GNSS technique, Dynamic force models with extended Kalman filter. PBD/POD method involves double differential GNSS technique, and application various force models like gravity, atmospheric drag, solar radiation pressure etc & extended Kalman Filter for the prediction.

The selected candidates will get an opportunity to work on this prestigious project.

Duration: 2 years.

Eligibility for JRF:

1. Junior Research Fellow:

Scale of pay: 37000.0; HRA - Type Z (18%) per month.

Essential: BTech/BE in EC/EE/AE/EI with CGPA of 8 or more.

Desirable: MTech/ME in EC/EE/AE, Industry experience of 0-2 years/ Very good knowledge in Python/C/ Matlab Programming.

How to apply?

Please visit www.cet.ac.in. Go to News session and click on [JRF Notification of the RESPOND project](#).

Interested applicants are requested to download the **application form** and fill in the details.

Next, register through the **registration** link provided. You need to upload the completed application form with the file name as your full name.

Last date for submitting the application form is 06th September 2024.

The applications will be scrutinized and only shortlisted candidates will be intimated through email to appear for an interview.

The interview will be held at Department of Electrical Engineering, College of Engineering Trivandrum, tentatively in the first week of September, 2024.